

Features

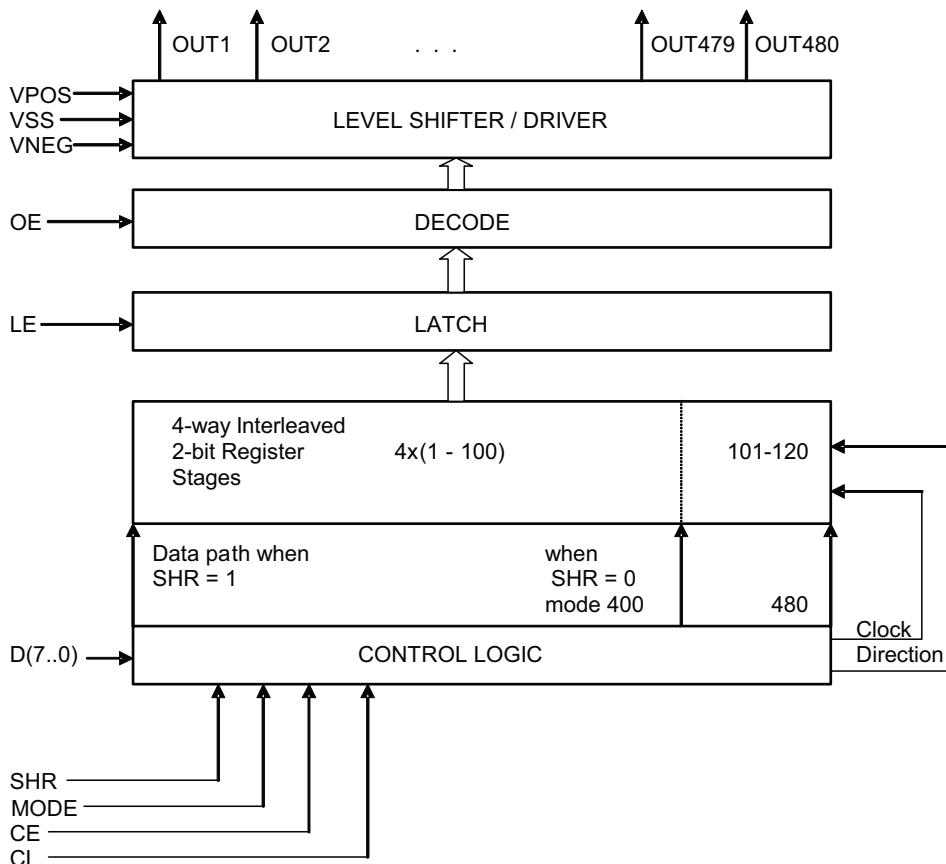
- CMOS Technology
- +/-15 Volt Output Driver Supply Voltage
- Drives Segment or Active Matrix Displays
- 4-Level Gray Scale
- 25MHz Clock Frequency
- Bidirectional Data Transfer
- Selectable Register Length
- 2.7V to 5.5V Logic Supply Voltage
- Cascadable

Ordering Information

Part	Description
EI148001-00	Gold Bumped Die / Waffle Pack
EI148002-00	Gold Bumped Die / Wafer Form



Figure 1. Functional Block Diagram



Description

The MXEI1480 is a selectable 400 or 480 bit long, 2-bit wide, serial-input parallel-output digital shift register with level conversion on each parallel output. The device converts the 2 digital bits into V_{POS} , V_{SS} , or V_{NEG} analog output voltages. An 8-bit input bus simultaneously inputs 4 groups of 2 bits each.

The MXEI1480 consists of a selectable length Bidirectional Input Register, Transfer Latch, and 480 bit Level Shifter / Output Driver. Each OUT pin is switched to one of [V_{SS} , V_{POS} , V_{NEG}] according to the D0...D7 logic levels clocked into the MXEI1480, modified by the OE pin.

The MXEI1480 is designed to operate over a temperature range of -40°C to +85°C, and is available as Gold Bumped Die in Waffle Pack, and Gold Bumped in Wafer Form.

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